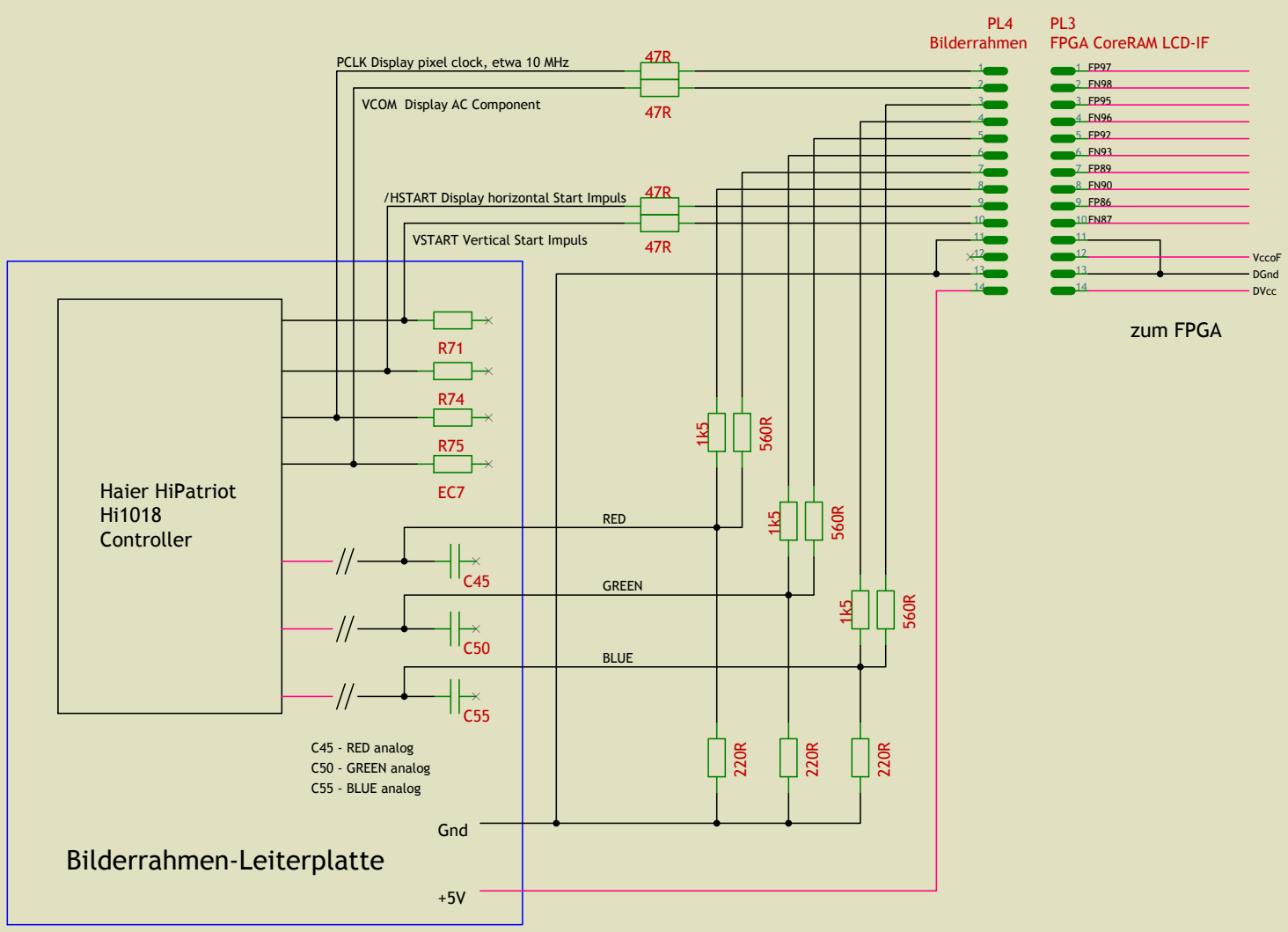

PL4
Bilderrahmen
PL3
FPGA CoreRAM LCD-IF
PCLK Display pixel clock, etwa 10 MHz
VCOM Display AC Component
/HSTART Display horizontal Start Impuls
VSTART Vertical Start Impuls
47R
47R
47R
47R
FP97
FN98
FP95
FN96
FP92
FN93
FP89
FN90
FP86
FN87
VccoF
DGnd
DVcc
zum FPGA
R71
R74
R75
EC7
Haier HiPatriot
Hi1018
Controller
1k5
560R
1k5
560R
1k5
560R
RED
GREEN
BLUE
C45
C50
C55
220R
220R
220R
C45 - RED analog
C50 - GREEN analog
C55 - BLUE analog
Gnd
+5V
Bilderrahmen-Leiterplatte